

Electronic Acknowledgement Receipt

EFS ID:	5185621
Application Number:	10586305
International Application Number:	
Confirmation Number:	2516
Title of Invention:	MANUFACTURING METHOD OF A SEMICONDUCTOR DEVICE HAVING ELECTRODES ON OPPOSED SIDES OF AN IC ELEMENT THEREOF AND FIRST AND SECOND CIRCUIT LAYERS ELECTRICALLY CONNECTED TO THE ELECTRODES
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Attorney Docket Number:	1204.46401X00
Receipt Date:	20-APR-2009
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Application Type:	U.S. National Stage under 35 USC 371

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RAM confirmation Number	4289
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Request for Continued Examination (RCE)	46401X00-RCE.pdf	132441	no	1
			120ac6626cfd9f018738bb0ae9ce9b201e1b74f7		
Warnings:					
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2	Amendment Submitted/Entered with Filing of CPA/RCE	46401X00-114Sub-Amd.pdf	98323	no	19
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Warnings:					
Information:					
3	Extension of Time	46401X00-2MoEOT.pdf	135177	no	1
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Warnings:					
Information:					
4	Transmittal Letter	46401X00-114Sub-IDS.pdf	73566	no	2
			1c8fc8977929f756bba686593881d939450c8f0b		
Warnings:					
Information:					
5	Information Disclosure Statement (IDS) Filed (SB/08)	46401X00-FormPTOSB08A.pdf	167095	no	1
			1162add947756f76dd7e552f6296dcc5827a35d8		
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6	NPL Documents	46401X00-Supp-ESR.pdf	99339	no	3
			ec109b9ed02ba5a7fb874af1e8b1b809d19b09c3		
Warnings:					
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7	Foreign Reference	46401X00-ForRef-1.pdf	1660664	no	24
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Warnings:					
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8	Foreign Reference	46401X00-ForRef-2.pdf	1191336	no	28
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9	Foreign Reference	46401X00-ForRef-3.pdf	680364 9b11563e58bae883d2f7b57e67185f0620d ff263	no	9
Warnings:					
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10	Foreign Reference	46401X00-ForRef-4.pdf	1044067 50e3ac53504cfb47044cced40b445dc6836f 8f1f	no	12
Warnings:					
Information:					
11	Fee Worksheet (PTO-06)	fee-info.pdf	32798 36122919f01124b68461ef878b8b5cca27a3 f4f9	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			5315170		
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